Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	137	(710/106.ccls.) and @ad<"19990608"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/01 12:57
L2	488	(710/305.ccls.) and @ad<"19990608"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/01 13:05
L3	229	(710/9.ccls.) and @ad<"19990608"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/01 13:12
L4	398	(710/10.ccls.) and @ad<"19990608"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/01 13:15
S1	1	("2346179".pn.) and @ad<"19990608"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/01 12:56
S2	2	("5841424".pn.) and @ad<"19990608"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/30 07:12
S3	4	cleary.xa.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/30 08:38
S4	1	"6600739".PN.	USPAT; USOCR	OR	ON	2004/11/30 07:13
S5	1	"6516367".PN.	USPAT; USOCR	OR	ON	2004/11/30 07:54
S6	1	"6505265".PN.	USPAT; USOCR	OR	ON	2004/11/30 07:55
S7	1	"6496893".PN.	USPAT; USOCR	OR	ON	2004/11/30 07:56
S8	1	"6415342".PN.	USPAT; USOCR	OR	ON	2004/11/30 07:58
S9	1	"6366964".PN.	USPAT; USOCR	OR	ON	2004/11/30 07:59

S10	1	"6263392".PN.	USPAT; USOCR	OR	ON	2004/11/30 08:06
S11	2	"6772252".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/30 08:39
S12	0	("6742055").URPN.	USPAT	OR	ON	2004/11/30 09:38
S13	135	(dynamic with endpoint) and @ad<"19990608"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/30 09:50
S14	4	((dynamic with endpoint) and usb) and @ad<"19990608"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/30 13:45
S15	1	"6134616".PN.	USPAT; USOCR	OR	ON	2004/11/30 13:39
S16	1	"6122676".PN.	USPAT; USOCR	OR	ON	2004/11/30 13:42
S17	1	"5619722".PN.	USPAT; USOCR	OR	ON	2004/11/30 13:43
S18	88	("6219736" "6219736" "6073205" "5935224" "5987530" "5991546" "6092210" "6105097" "6119195" "6191713" "5799196" "5832492" "5860119" "5896534" "5911152" "5933611" "5943506" "5963431" "5974486" "6044428" "6058441" "6061687" "6061746" "6061746" "6061687" "6101076" "6145029" "6148354" "6151645" "6185641" "6216183" "6222716" "6226696" "6216183" "6222716" "6226696" "4398687" "4463920" "5400404" "5432405" "5757217" "5767844" "5774744" "5778218" "5781028" "5784581" "5787259" "5800192" "5808481" "5818948").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/30 13:45

60
PLUS Search Results for S/N 09786560, Searched November 30, 2004

The Patent Linguistics Utility System (PLUS) is a USPTO automated sear
ch
system for U.S. Patents from 1971 to the present. PLUS is a
query-by-example search system which produces a list of patents that a
re
most closely related linguistically to the application searched. This
search was prepared by the staff of the Scientific and Technical
Information Center, SIRA.

6219736
6219736
6073205
5935224
5987530
5991546
6092210
6105097
6119195
6191713
5799196
5832492
5860119
5896534
5911152
5933611
5943506
5963431
5974486
6044428
6058441
6061687
6061746
6061746
6061687
6101076
6145029
6148354
6151645
6185641
6216183
6222716
6226696
6216183
6222716

6226696
4398687
4463920
5400404
5432405
5757217
5767844
5774744
5778218
5781028
5784581
5787259
5800192
5808481
5818948

```
6219736 99
6219736 99
6073205 93
5935224 90
5987530 90
5991546 90
6092210 90
6105097 90
6119195 90
6191713 90
5799196 88
5832492 88
5860119 88
5896534 88
5911152 88
5933611 88
5943506 88
5963431 88
5974486 88
6044428 88
6058441 88
6061687 88
6061746 88
6061746 88
6061687 88
6101076 88
6145029 88
6148354 88
6151645 88
6185641 88
6216183 88
6222716 88
6226696 88
6216183 88
6222716 88
6226696 88
4398687 86
4463920 86
5400404 86
5432405 86
5757217 86
5767844 86
5774744 86
5778218 86
5781028 86
5784581 86
5787259 86
5800192 86
```

5808481 86
5818948 86

Most Frequently Occurring Classifications of Patents Returned
From A Search of 09786560 on November 30, 2004

Original Classifications
    4   710/100
    3   707/101
    2   244/207
    2   361/103
    2   710/10
    2   710/315
    2   710/63
    2   710/8

Cross-Reference Classifications
    5   710/52
    4   710/14
    4   710/62
    4   710/8
    3   707/100
    3   707/102
    3   710/100
    3   710/2
    2   239/265.23
    2   307/125
    2   345/161
    2   345/163
    2   361/93.2
    2   361/93.8
    2   361/94
    2   370/259
    2   370/420
    2   370/524
    2   416/20R
    2   416/90A
    2   709/226
    2   709/250
    2   710/104
    2   710/22
    2   710/260
    2   710/302
    2   710/313
    2   710/5
    2   710/63
    2   711/163
    2   713/200
    2   713/202
    2   713/310

```
2  719/321
2  719/328
```

Combined Classifications
```
7  710/100
6  710/8
5  710/52
5  710/62
4  710/14
4  710/63
3  707/100
3  707/101
3  707/102   .
3  710/2
3  710/315
2  239/265.23
2  244/207
2  307/125
2  326/30
2  345/161
2  345/163
2  361/103
2  361/93.2
2  361/93.8
2  361/94
2  370/259
2  370/420
2  370/524
2  416/20R
2  416/90A
2  709/226
2  709/250
2  710/10
2  710/104
2  710/22
2  710/260
2  710/302
2  710/313
2  710/4
2  710/5
2  710/53
2  711/163
2  713/200
2  713/202
2  713/310
2  713/320
2  713/400
2  719/321
```

2   719/328

Titles of Most Frequently Occurring Classifications of Patents Returne
d

From A Search of 09786560 on November 30, 2004


```
7  710/100      (4 OR, 3 XR)
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                         PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/100         INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                         TRANSACTION PROCESSING)


6  710/8        (2 OR, 4 XR)
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                         PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1           INPUT/OUTPUT DATA PROCESSING
        710/8           .Peripheral configuration


5  710/52       (0 OR, 5 XR)
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                         PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1           INPUT/OUTPUT DATA PROCESSING
        710/52          .Input/Output data buffering


5  710/62       (1 OR, 4 XR)
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                         PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1           INPUT/OUTPUT DATA PROCESSING
        710/62          .Peripheral adapting


4  710/14       (0 OR, 4 XR)
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                         PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1           INPUT/OUTPUT DATA PROCESSING
        710/8           .Peripheral configuration
        710/14          ..Mode selection


4  710/63       (2 OR, 2 XR)
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                         PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1           INPUT/OUTPUT DATA PROCESSING
        710/62          .Peripheral adapting
        710/63          ..Universal


3  707/100      (0 OR, 3 XR)
        Class   707 :  DATA PROCESSING:  DATABASE AND FILE
                         MANAGEMENT OR DATA STRUCTURES
        707/100         DATABASE SCHEMA OR DATA STRUCTURE
```

```
3  707/101      (3 OR, 0 XR)
     Class    707 :  DATA PROCESSING:  DATABASE AND FILE
                     MANAGEMENT OR DATA STRUCTURES
          707/100       DATABASE SCHEMA OR DATA STRUCTURE
          707/101       .Manipulating data structure (e.g.,
                          compression, compaction, compilation)


3  707/102      (0 OR, 3 XR)
     Class    707 :  DATA PROCESSING:  DATABASE AND FILE
                     MANAGEMENT OR DATA STRUCTURES
          707/100       DATABASE SCHEMA OR DATA STRUCTURE
          707/102       .Generating database or data structure (e.g.,
                          via user interface)


3  710/2        (0 OR, 3 XR)
     Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                     PROCESSING SYSTEMS:  INPUT/OUTPUT
          710/1         INPUT/OUTPUT DATA PROCESSING
          710/2         .Input/Output expansion


3  710/315      (2 OR, 1 XR)
     Class    710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                     PROCESSING SYSTEMS:  INPUT/OUTPUT
          710/100       INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                          TRANSACTION PROCESSING)
          710/305       .Bus interface architecture
          710/306       ..Bus bridge
          710/315       ...Different protocol (e.g., PCI to ISA)


2  239/265.23   (0 OR, 2 XR)
     Class    239 :  FLUID SPRINKLING, SPRAYING, AND DIFFUSING
          239/265.11    REACTION MOTOR DISCHARGE NOZZLE
          239/265.19    .With means controlling amount, shape or
                          direction of discharge stream
          239/265.23    ..Fluid jet for stream deflection


2  244/207      (2 OR, 0 XR)
     Class    244 :  AERONAUTICS
          244/34R       AIRCRAFT SUSTENTATION
          244/35R       .Sustaining airfoils
          244/198       ..With lift modification
          244/201       ...Variable
          244/204       ....By controlling boundary layer
          244/207       .....With blowing


2  307/125      (0 OR, 2 XR)
     Class    307 :  ELECTRICAL TRANSMISSION OR INTERCONNECTION
                     SYSTEMS
```

```
            307/112        SWITCHING SYSTEMS
            307/116        .Condition responsive
            307/125        ..Electrical


  2  326/30        (1 OR, 1 XR)
            Class     326 :  ELECTRONIC DIGITAL LOGIC CIRCUITRY
            326/21         SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY
            326/30         .Bus or line termination (e.g., clamping,
                              impedance matching, etc.)


  2  345/161       (0 OR, 2 XR)
            Class     345 :  COMPUTER GRAPHICS PROCESSING, OPERATOR
                              INTERFACE PROCESSING, AND SELECTIVE VISUAL
DISPLAY
                           SYSTEMS
            345/156        DISPLAY PERIPHERAL INTERFACE INPUT DEVICE
            345/157        .Cursor mark position control device
            345/161        ..Joystick


  2  345/163       (0 OR, 2 XR)
            Class     345 :  COMPUTER GRAPHICS PROCESSING, OPERATOR
                              INTERFACE PROCESSING, AND SELECTIVE VISUAL
DISPLAY
                           SYSTEMS
            345/156        DISPLAY PERIPHERAL INTERFACE INPUT DEVICE
            345/157        .Cursor mark position control device
            345/163        ..Mouse


  2  361/103       (2 OR, 0 XR)
            Class     361 :  ELECTRICITY:  ELECTRICAL SYSTEMS AND DEVICES

            361/1          SAFETY AND PROTECTION OF SYSTEMS AND DEVICES
            361/103        .Circuit interruption by thermal sensing


  2  361/93.2      (0 OR, 2 XR)
            Class     361 :  ELECTRICITY:  ELECTRICAL SYSTEMS AND DEVICES

            361/1          SAFETY AND PROTECTION OF SYSTEMS AND DEVICES
            361/93.1       .With specific current responsive fault sensor

            361/93.2       ..Digital control


  2  361/93.8      (0 OR, 2 XR)
            Class     361 :  ELECTRICITY:  ELECTRICAL SYSTEMS AND DEVICES

            361/1          SAFETY AND PROTECTION OF SYSTEMS AND DEVICES
            361/93.1       .With specific current responsive fault sensor
```

```
        361/93.8        ..Thermal sensing

2   361/94          (0 OR, 2 XR)
        Class   361 :   ELECTRICITY:  ELECTRICAL SYSTEMS AND DEVICES

        361/1           SAFETY AND PROTECTION OF SYSTEMS AND DEVICES
        361/93.1        .With specific current responsive fault sensor

        361/94          ..With time delay protective means

2   370/259         (0 OR, 2 XR)
        Class   370 :   MULTIPLEX COMMUNICATIONS
        370/259         SPECIAL SERVICES

2   370/420         (0 OR, 2 XR)
        Class   370 :   MULTIPLEX COMMUNICATIONS
        370/351         PATHFINDING OR ROUTING
        370/389         .Switching a message which includes an address
                            header
        370/419         ..Input or output circuit, per se (i.e., line
                            interface).
        370/420         ...For connecting plural subscribers to a
                            network (i.e., network termination)

2   370/524         (0 OR, 2 XR)
        Class   370 :   MULTIPLEX COMMUNICATIONS
        370/473         ..Transmission of a single message having
                            multiple packets
        370/498         .Combining or distributing information via tim
e
                            channels
        370/522         ..Signaling (ancillary to main information)
        370/524         ...Using a dedicated signaling channel (i.e.,
                            D-channel)

2   416/20R         (0 OR, 2 XR)
        Class   416 :   FLUID REACTION SURFACES
        416/20R         DRIVE BY FLUID REACTION JET ON WORKING MEMBER

2   416/90A         (0 OR, 2 XR)
        Class   416 :   FLUID REACTION SURFACES
        416/90R         WITH FLUID PASSAGE IN WORKING MEMBER
                            COMMUNICATING WITH WORKING FLUID
        416/90A         .Air and watercraft propellers

2   709/226         (0 OR, 2 XR)
        Class   709 :   ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
```

```
                        09786560_CLSTITLES
                        SYSTEMS:  MULTIPLE COMPUTER OR PROCESS    COO
RDINATING
         709/200          MULTICOMPUTER DATA TRANSFERRING
         709/223          .Computer network managing
         709/226          ..Network resource allocating


  2  709/250        (0 OR, 2 XR)
         Class   709 :  ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                        SYSTEMS:  MULTIPLE COMPUTER OR PROCESS    COO
RDINATING
         709/200          MULTICOMPUTER DATA TRANSFERRING
         709/250          .Network-to-computer interfacing


  2  710/10         (2 OR, 0 XR)
         Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                        PROCESSING SYSTEMS:  INPUT/OUTPUT
         710/1            INPUT/OUTPUT DATA PROCESSING
         710/8            .Peripheral configuration
         710/10           ..Configuration initialization


  2  710/104        (0 OR, 2 XR)
         Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                        PROCESSING SYSTEMS:  INPUT/OUTPUT
         710/100          INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                            TRANSACTION PROCESSING)
         710/104          .System configuring


  2  710/22         (0 OR, 2 XR)
         Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                        PROCESSING SYSTEMS:  INPUT/OUTPUT
         710/1            INPUT/OUTPUT DATA PROCESSING
         710/22           .Direct Memory Accessing (DMA)


  2  710/260        (0 OR, 2 XR)
         Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                        PROCESSING SYSTEMS:  INPUT/OUTPUT
         710/260          INTERRUPT PROCESSING


  2  710/302        (0 OR, 2 XR)
         Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                        PROCESSING SYSTEMS:  INPUT/OUTPUT
         710/100          INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                            TRANSACTION PROCESSING)
         710/300          .Bus expansion or extension
         710/301          ..Card insertion
         710/302          ...Hot insertion


  2  710/313        (0 OR, 2 XR)
```

```
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                           PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/100            INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                               TRANSACTION PROCESSING)
        710/305            .Bus interface architecture
        710/306            ..Bus bridge
        710/313            ...Peripheral bus coupling (e.g., PCI, USB,
                               ISA, and etc.)


2  710/4          (1 OR, 1 XR)
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                           PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1              INPUT/OUTPUT DATA PROCESSING
        710/3              .Input/Output addressing
        710/4              ..Address data transfer


2  710/5          (0 OR, 2 XR)
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                           PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1              INPUT/OUTPUT DATA PROCESSING
        710/5              .Input/Output command process


2  710/53         (1 OR, 1 XR)
        Class   710 :  ELECTRICAL COMPUTERS AND DIGITAL DATA
                           PROCESSING SYSTEMS:  INPUT/OUTPUT
        710/1              INPUT/OUTPUT DATA PROCESSING
        710/52             .Input/Output data buffering
        710/53             ..Alternately filling or emptying buffers


2  711/163        (0 OR, 2 XR)
        Class   711 :  ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                           SYSTEMS: MEMORY
        711/100            STORAGE ACCESSING AND CONTROL
        711/154            .Control technique
        711/163            ..Access limiting


2  713/200        (0 OR, 2 XR)
        Class   713 :  ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                           SYSTEMS:  SUPPORT
        713/200            SECURITY


2  713/202        (0 OR, 2 XR)
        Class   713 :  ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                           SYSTEMS:  SUPPORT
        713/200            SECURITY
        713/202            .Password


2  713/310        (0 OR, 2 XR)
```

Class   713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                    SYSTEMS: SUPPORT
713/300   COMPUTER POWER CONTROL
713/310   .By external command

2   713/320   (1 OR, 1 XR)
    Class   713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                    SYSTEMS: SUPPORT
    713/300   COMPUTER POWER CONTROL
    713/320   .Power conservation

2   713/400   (1 OR, 1 XR)
    Class   713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                    SYSTEMS: SUPPORT
    713/400   SYNCHRONIZATION OF CLOCK OR TIMING SIGNALS,
                        DATA, OR PULSES

2   719/321   (0 OR, 2 XR)
    Class   719 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                        SYSTEMS: INTERPROGRAM COMMUNICATION OR INTER
PROCESS
                        COMMUNICATION (IPC)
    719/321   DEVICE DRIVER COMMUNICATION

2   719/328   (0 OR, 2 XR)
    Class   719 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                        SYSTEMS: INTERPROGRAM COMMUNICATION OR INTER
PROCESS
                        COMMUNICATION (IPC)
    719/328   APPLICATION PROGRAM INTERFACE (API)